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Research Article

Electrical and morphological variations with sintering temperature of electron transport layer in perovskite solar cell

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Abstract. The typical PSCs essentially made up of electron transporting material (compact and mesoporous), perovskite absorber layer and hole transporting material. The compact TiO2 primary function is allow the movement of photogenerated electron to the device circuit from the active layer and to block the photogenerated holes from recombination at TCO substrate. Mesoporous TiO2 mainly functions to receive the photogenerated charge from the perovskite absorber and enable thicker formation of perovskite absorber due to the voids in the TiO2 mesoscopic framework. Many studies have implemented 500 °C as the standard in sintering the TiO2 layer. However, the effects of sintering temperature of ETL TiO2 have never been systematically described in terms of morphology and photoelectrochemical properties. In this manuscript, we have studied the morphological and photoelectrochemical properties of ETM TiO2 thin film prepared at different sintering temperature. Spin coated TiO2 layers were examined using X-ray Diffraction for crystal structure and phase identification, FESEM for morphological analysis, UV-Vis Spectroscopy for optical absorbance and transmittance of light and PEC test for LSV, EIS and TPC analyses. Surface roughness was not a major influencing factor of photocurrent density rather than the anatase phase of the TiO2 thin film is more important. It was revealed that at 500 °C, the TiO2 thin film possess the highest photocurrent density with good stability and lowest charge transfer and series resistance. Higher sintering temperature of 550 °C, would introduce lattice defects in the TiO2 thin film which will reduce photocurrent density and increase resistance. This work offers a systematic evaluation of the ETL in terms of morphological and photoelectrochemical properties, which can be applied when selecting suitable material for ETL in perovskite solar devices.

Keywords: perovskite solar cells; electron transport layer; TiO2; sintering temperature; charge transfer resistance



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1. Introduction

Perovskite solar cells (PSCs) have been considered one of the most promising solar cell technologies because of its high photoconversion efficiency, which rivals that of high strainfabricated crystalline silicon solar cells (Abd Mutalib, Aziz, et al., 2018). Silicon-based solar cells have traditionally dominated the market, but metal halide PSCs have quickly emerged as a highly promising alternative (Han et al., 2025). Though still relatively new, PSCs are advancing at an exceptional pace, propelled by intensive global research efforts that leverage their distinctive advantages. These promising cells boast lower production costs, simpler fabrication methods, and superior mechanical flexibility compared to conventional silicon-based technologies.

Despite the presence of lead and the moisture susceptibility of the perovskite absorber layer (Abd Mutalib, Ahmad Ludin, et al., 2018), the power conversion efficiency of PSCs has been on a steady ascension from 9% to 24.2% ("NREL Best Research-Cell Efficiencies," n.d.). Typical PSCs are made of electron transport layer (ETL; compact and mesoporous), perovskite absorber layer and hole transport layer (HTL). Nevertheless, in planar PSC design, where mesoporous ETL is omitted, is also sometimes applied allowing low temperature processing of the

PSC (Abd Mutalib, Ahmad Ludin, et al., 2018). Past studies have outlined the optimum properties of primary materials for highperformance ETL. These properties include good electron mobility with low photocurrent resistance, wide band gap for high light transmission and compatible energy level with other PCS component layers (Lu, Ma, Gu, Tian, & Li, 2015). In addition, high light transmission can be achieved by the application of ETL material with low reflection to prevent light scattering. Compact TiO2 (c-TiO2) allows the movement of photogenerated electron from the active layer to the device circuit and blocks the recombination of photogenerated holes at the transparent conductive oxide (TCO) substrate (Wu et al., 2015). The ETL must be continuous, uniform and thin to avoid the charge recombination between the perovskite layer and TCO substrate and prevent current leakage (Lee et al., 2013). Thus, c-TiO₂ is imperative to produce highly efficient PSCs.

Later, new PSC structures have emerged. Such new PSCs include planar PSC, in which the mesoporous TiO₂ (m-TiO₂) layer is omitted from the structure (Liu, Johnston, & Snaith, 2013). The importance of m-TiO2 was later revealed, that is, it receives the photogenerated charge from the perovskite absorber and enables the formation of thicker perovskite absorber because of the voids in the TiO2 mesoscopic

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framework (Mohamad Firdaus Mohamad Noh et al., 2018). The mesoporous structure provides a high surface area, which increases the charge transfer rate between the perovskite material and ETL (Meyer, 2010). Moreover, hysteresis effect mitigation and higher performance stability were achieved in PCS devices with m-TiO₂ compared with planar PCSs.

Different methods of ETL deposition have been widely discussed and compared to date. ETL TiO2 have been fabricated using various methods, including spin coating (Abd Mutalib et al., 2022; Im, Jang, Pellet, Grätzel, & Park, 2014), spray pyrolysis (Saliba et al., 2016), screen printing (Mahmood, Swain, & Amassian, 2015), sputtering (Chen, Cheng, Dai, & Song, 2015), atomic layer deposition and electrochemical deposition (Kavan, Tétreault, Moehl, & Grätzel, 2014). Most research have adopted the spin coating method probably because of its cost effectiveness, simple process and ease of use. The sintering temperature applied in spin coating is usually ~500 °C, which usually aimed at producing anatase TiO₂ (Green, Ho-Baillie, Snaith, & Martin A. Green, 2014). However, the effects of the sintering temperature of ETL TiO₂ have never been systematically described in terms of morphological and photoelectrochemical (PEC) properties.

In the effort to avoid high fabrication cost due to high energy requirement from high temperature (<450 °C), some have researched low energy requirement fabrication method (Im and Olasoji, 2024). Nevertheless, the resulting PCE from low temperature devices still cannot match the convention temperature treatment methods (Yoo et al., 2024). However, recent advancements in low-temperature deposition techniques have enabled the fabrication of perovskite solar devices on flexible substrates, marking a significant step toward lightweight, portable, and versatile photovoltaic technologies. These innovative methods, which typically operate below 150 °C, are compatible with polymer-based substrates that would otherwise degrade under conventional high-temperature processing. This breakthrough has opened new avenues for integrating perovskite solar cells into wearable electronics, rollto-roll manufacturing, and building-integrated photovoltaics. Nevertheless, despite the promising potential, devices fabricated on flexible substrates generally exhibit lower performance metrics (reduced power conversion efficiency and operational stability), when compared to their counterparts built on rigid, thermally robust substrates like glass. This performance gap is often attributed to challenges such as poor film uniformity, suboptimal crystal quality, and increased defect densities that arise during low-temperature processing. As such, ongoing research is focused on optimizing deposition parameters and material formulations to enhance the efficiency and reliability of flexible perovskite solar devices.

In this manuscript, we studied the morphological and PEC properties of ETL TiO_2 thin film prepared at different sintering temperatures, namely, 450, 500 and 550 °C. The morphology and PEC properties of the spin-coated ETL TiO_2 were studied thoroughly. Spin coating method was chosen because of its simplicity, cost and ease of use. The results showed that the TiO_2 thin film sintered at 500 °C possessed the highest photocurrent density, good stability and lowest charge transfer and series resistance.

2. Material and Method

2.1 TiO2 layer preparation

 ${
m TiO_2}$ BL-1 blocking layer, 18NR-T Transparent Titania Paste and fluorine-doped tin oxide (FTO)-coated glass were

purchased from Greatcell Solar Materials (greatcellsolarmaterials.com). The FTO-coated glass substrates were etched to draw out the desired electrode pattern using zinc powder and HCl (3 M). The glass substrates were then washed consecutively with soap (2% Hellmanex in water), acetone and isopropanol to remove the organic residue on their surface. The chemicals were obtained from Sigma Aldrich and used as received. Firstly, approximately 100 nm c-TiO2 layer was spincoated at the surface of the active layer using BL-1 mixed with ethanol as the precursor and then heated to 150 °C. The m-TiO₂ layer (~400 nm) was then spin-coated using 18 NR-T paste and then sintered at different temperatures for 60 min. Both c-TiO₂ and m-TiO2 were spin-coated at 3000 r/min for 60 s. The TiO2 thin films sintered at different temperatures (450, 500 and 550 °C) are herein denoted as according to their sintering temperature.

2.2 Characterization

The characterization part in this manuscript is consisted of physical, optical and PEC characterizations. Physical characterization consists of X-ray diffraction (XRD), Atomic Force Microscopy (AFM) and Field-emission scanning electron microscope (FESEM). Optical characterization consists of UV–visible spectrophotometer. Finally, the PEC characterization consists of linear sweep voltammetry (LSV) and electrochemical impedance spectroscopy (EIS).

2.3 Physical Characterization

The crystal structure of the TiO2 thin films was determined via XRD using a Bruker D8 Advance operated at 2θ angle in the range of 20°-70°. The crystal structure of the TiO2 has to be identified because one of the polymorphs has the best properties for electron transport layer application. The data findings were then processed using Diffrac Suite EVA software evaluation to obtain crystallinity percentage of the samples. The thickness of the films was measured using a profilometer (Dektak XT, Bruker). The surface roughness of the TiO2 thin films is measured using Nanosurf Easyscan 2 AFM by Park Systems. A variation in surface roughness may serve as an indicator of alterations in the crystal structure of the sample. Such changes often reflect underlying modifications in the material's microstructure, which can arise from differences in processing conditions, phase transitions, or the incorporation of dopants or defects. The surface morphology of the TiO2 films were analysed using a FESEM using a SUPRA VP55.

2.4 Optical Characterization

Specular transmittance measurements were carried out at room temperature, in the wavelength range of 300-1000 nm, using a UV-visible spectrophotometer (Lambda 35, PerkinElmer) to investigate the optical properties of the samples. The optical properties will allow to inspect over the absorption and transmittance range the samples with different sintering temperature and also detects the change in band gap (eV). The band gap values also allow the detection crystal structure of TiO_2 as compared to previous study.

2.5 Photoelectrochemical Characterization

The PEC properties of the TiO_2 films were determined via LSV and EIS using an electrochemical workstation (Metrohm Autolab) in the dark and under visible light illumination (simulated AM 1.5 at a calibrated intensity 100 mW/cm²) at room temperature and atmospheric pressure. The TiO_2 thin

film-coated FTO substrate with an active area of 1.0 cm² was dipped in 40 ml of aqueous 0.5 M $\rm Na_2SO_4$ solution and connected to the workstation as the working electrode. Pt electrode was used as the counter electrode, and Ag/AgCl electrode was used as the reference electrode (RE). For LSV measurement, a scanning rate of 10 mV/s, in the potential range of -0.4 – 1.4 V with respect to RE, was applied. EIS measurement was carried out at the frequency range of 105 – 0.1 Hz and the bias voltage of 0.0 V with respect to RE.

3. Results and Discussion

3.1 X-ray Diffraction

The XRD patterns of all the TiO2 thin films were determined to examine the changes in crystallinity and crystallite size, with respect to temperature variation between 450 °C to 550 °C. Higher crystallinity would allow higher charge transfer along the thin film with lower probability to encounter crystal defects. It was proven that TiO₂ with anatase crystal structure is a preferred TiO2 forms rather than rutile crystal structure due to the epitaxial growth of TiO2 grains producing higher electron mobility in anatase form (Luttrell et al., 2015). While rutile TiO₂ is thermodynamically stable at high temperatures, anatase TiO2 is typically preferred for photo-related applications due to its higher surface area, better electron mobility, and longer electron lifetime, which contribute to enhanced photocatalytic and photovoltaic performance (Luttrell et al., 2014). Additionally, when using spin coating technique to deposit the perovskite active layer, the perovskite precursor solution can be easily deposited onto a highly crystalline ETL (Ye et al., 2017). As Figure 1 suggests, all samples have no remarkable changes in XRD pattern and exhibited the typical anatase crystal structure of TiO₂ diffraction peaks at 25.7° (101), 38.2° (004), 48.4° (200) and 55.0° (211) (JCPDS card no. 21-1272). Additional XRD peaks attributed to the FTO underlayer is depicted in Fig. 1.

The Scherrer equation was applied to the main diffraction peaks in the diffraction pattern and averaged to estimate the crystalline size of the deposited TiO_2 thin film.

$$D = \frac{K\lambda}{\beta \cos \theta} \tag{1}$$

where D is the average crystallite size in angstroms (Å), K is the shape factor taken as 0.9, λ is the wavelength of X-ray radiation (1.5406 Å), β is the FWHM after appropriate base line correction, and θ is the diffraction angle. The prepared samples were differentiated according to degree of crystallinity and crystallite size. Table 1 shows the crystallite size, crystallinity percentage, layer thickness and surface roughness of all samples. As described in the table, the crystallite size and crystallinity percentage tend to increase as the temperature increases. As revealed in previous study, the increase in crystallite size is an indication of structural change from anatase to rutile, suggesting the crystallite growth according to the Ostwald-ripening mechanism (Chao, Petrovsky, & Dogan, 2010). Additionally, the atomic force microscopy and profilometer results revealed an increase in the surface roughness and layer thickness of TiO2-550.

Surface roughness plays an important role towards the performance of photoanode thin film, where previous study have elaborated the increase in surface roughness would lead to higher number of active photocatalytic sites on the surface (Noh et al. 2018). The surface roughness of a thin film is closely associated with the availability of photocatalytic sites, which influences photocurrent performance. Previous studies have explained that the increased surface roughness is a result of the unique grain behavior of rutile TiO₂. The rutile phase tends to exhibit more irregular grain growth, leading to larger, more pronounced surface features and grain boundaries, which

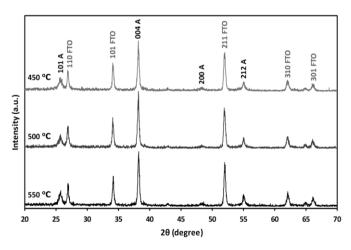


Fig. 1 XRD patterns of TiO₂ thin films sintered at different temperatures.

 Table 1

 Crystallite size and crystallinity percentage of the prepared samples

Temperature, °C	Crystallite size, nm	Crystallinity percentage, %	Layer thickness, nm	Surface roughness, nm
450	27.38	85.7	538.4	4.9
500	30.05	86.4	576.3	5.0
550	31.79	86.7	620.1	17.3

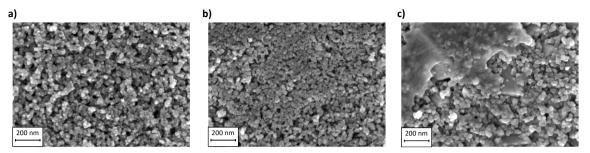


Fig. 2 FESEM images of the surface of (a) TiO₂-450, (b) TiO₂-500 and (c) TiO₂-550.

contribute to the enhanced roughness (Luttrell *et al.*, 2014). The grains exhibit a mildly rectangular morphology, with two distinct types oriented perpendicular to each other at 90°. The increase in surface roughness at different sintering temperatures was further analysed with the photocurrent performance of TiO_2 thin film.

3.2 Surface Morphology

The morphological properties of the spin-coated TiO₂ thin films sintered at different temperatures were examined by FESEM. Figure 2 shows the top view of the prepared TiO₂ films. The surfaces of the TiO2 thin films have good coverage over the substrate surface without the presence of cracks and with a relatively porous structure. The compactness of the layer increased (i.e. decrease in porosity) as the sintering temperature increased. A similar effect was reported in a previous study, in which a higher degree of film compactness was successfully achieved through the use of magnetron sputtering. This technique facilitated the formation of a denser and more uniform layer, thereby enhancing the material's structural and functional properties, which are critical for improving device performance and stability (Zhang et al., 2016). Most importantly, in Figure 2(c), the surface of TiO₂ thin film starts to agglomerate while exposing the underlayer features (c-TiO₂) and diminishing the porous structure of the thin film. The TiO2 layer maintains its anatase structural stability at 450-500 °C and partly transforms to rutile phase at 550 °C (Gao et al., 2008). Thus, Figure 2(c) shows some indication of phase change (agglomeration) at the surface of the TiO2 thin film. High temperature endorses agglomeration and pore collapse and thus diminishes the active surface areas of the thin film (Gomes et al., 2019). Likewise, later, the UV-Vis results have indicated

the phase change of anatase to rutile by the change in the band gap of the thin film.

3.3 Optical Properties

One of the key requirements for achieving a high-performance electron transport layer (ETL) in perovskite solar cells is the use of a material that possesses a wide band gap and exhibits high optical transparency. This characteristic is essential to ensure minimal parasitic absorption within the ETL itself, thereby allowing maximum transmission of incident light to the underlying perovskite absorber layer, which is critical for efficient photon harvesting and overall device performance (Mohamad Firdaus Mohamad Noh et al., 2018b). The optical properties of the prepared TiO2 thin film sample were examined using UV-Vis spectrometer and are shown in Figure 3. All absorption spectra show typical TiO2 absorption, increasing as it approaches the UV region (Yang et al., 2017). The light absorption ability of the thin films increased with the sintering temperature. This phenomenon was portrayed in Figure 3(a), where higher light absorption would translate to less photons reaching to the light absorbing layer (perovskite). The inset in Figure 3(a) shows the Tauc's plot, which was used to estimate the band gap energy of the TiO₂ thin films. The band gap displayed a descending pattern as the sintering temperature increases. The band gap values of TiO2-450, TiO2-500 and TiO2-550 °C were 3.25, 3.18 and 3.0 eV, respectively. The band gap energy values were relatively similar with previous study where the band gap values are correlated with the phase of TiO2 (anatase at 3.27 eV or rutile at 2.95 eV) (Yang et al., 2017). Notably, the band gap values confirmed the phase change of the TiO2 thin film from anatase to rutile with the increase in sintering temperature from 500 °C to 550 °C.

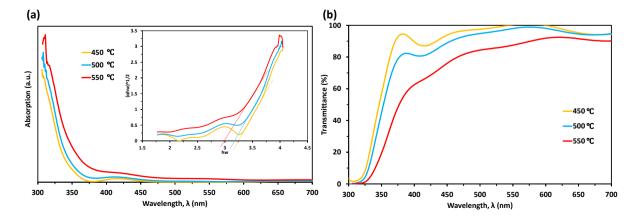


Fig. 3 (a) Absorption and (b) transmission spectra of the TiO2 thin films sintered at different temperatures.

The light transmission spectra of the prepared TiO2 thin films are shown in Figure 3(b). All the transmittance spectra showed typical TiO_2 thin film response with low transparency at the UV range of the wavelength, as expected due to the nature of the material with such bandgap (Chibani, Challali, Touam, Chelouche, & Djouadi, 2019). The figure shows a reduction in light transmission as the TiO2 sintering temperature increased. The reduction in light transmission may be related to the higher absorbance and layer thickness in higher sintering temperature samples. Additionally, high surface roughness at 550 °C could also contribute to lower transparency as similarly reported in a previous study (Soh-Yusoff et al., 2019). Surface irregularities lead to the scattering of incident light, thereby reducing the intensity of the transmitted light. This phenomenon has been previously documented and analyzed in earlier studies, which highlight the significant impact of surface morphology on light transmission efficiency (Li et al., 2004).

3.4 Photoelectrochemical Properties

In the photoelectrochemical properties evaluation, the sintering temperatures of TiO2 thin film is set with 50 °C interval between the samples (450 °C, 500 °C and 550 °C). PEC performance test was set up on a three-electrode configuration to evaluate the charge transport properties of the deposited TiO₂ thin films prepared at different sintering temperatures, and the results are shown in Figure 4. The low light absorption of TiO₂ thin film decreased the photoinduced charge carrier generation in the thin film. Based on the figure, the highest photocurrent density at 1.0 V was observed in TiO2-500 with 104.67 µA/cm² as shown in Table 2. This result indicates that while surface roughness can influence device performance, it is not the sole contributing factor responsible for the enhancement of photocurrent density. Other parameters, such as film crystallinity, interface quality, charge transport properties, and the presence of defects or trap states, may also play significant roles in determining the overall photoresponse of the device.

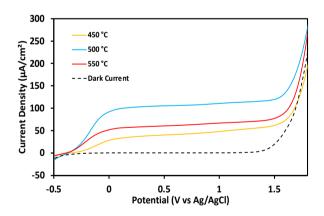


Fig. 4 Photocurrent density versus potential curves of the sintered TiO₂ thin films.

 $\begin{tabular}{ll} \textbf{Table 2} \\ Photocurrent density of TiO_2 thin films sintered at different temperatures \\ \end{tabular}$

TiO ₂ sintering temperature, °C	Photocurrent density µA/cm²	
450	50.41	
500	104.67	
550	55.58	

The difference in the photocurrent densities between TiO_2 -450 and TiO_2 -500 is closely related to the increase in crystallinity percentage, the reduction in crystallite size and the increase in layer compactness. However, the decrease in the photocurrent density of TiO_2 -550 may be related to the formation of aggregates (crystal defects) in the TiO_2 thin film, which precedes the phase transformation of TiO_2 . These aggregates, albeit produced high surface roughness, can introduce lattice defects, which can become charge recombination sites in the thin film and reduce the photocurrent generation of the thin film (Wu *et al.*, 2014).

EIS measurement was performed to determine the effects of temperature on the charge transfer characteristic of the thin films. The Nyquist plot of all TiO2 thin films measured in the presence of light at 0 V bias voltage is shown in Figure 5. In general, the Nyquist plot displays at least one semicircle, which corresponds to an [R(RC)] component. Charge transfer resistance (RCT) is represented by the diameter of the semicircle arc. Thus, a larger semicircle diameter means an increase in RCT, which is inversely proportional to the photocurrent. Based on Figure 5 and Table 3, TiO2 thin film sintered at 500 °C had the lowest RCT, and highest RCT at 550 °C. This finding corresponds with the LSV results. The increase in the RCT at temperature 550 °C and above may be related to the phase change at high sintering temperature (~550°C). Additionally, the increase in RCT and series resistance (RS) may be associated with the increase in layer thickness (refer Table 1). This increase in resistance would lead to the low current density and fill factor of perovskite solar device (Noh et al., 2018). The TiO2-500 thin film had the lowest RCT may be due to the anatase phase of the TiO2 thin film. A previous study reported similar findings that rutile-based TiO2 thin film has lower electron transport property compared with anatase-based thin film (Yang et al., 2017). The combination of a lower effective electron mass and an extended electron lifetime plays a crucial role in suppressing recombination processes within anatase thin films. The reduced effective mass facilitates more efficient charge carrier mobility, allowing electrons to traverse the material with less resistance. Simultaneously, the longer electron lifetime provides an extended window during which charge carriers can be collected before recombination occurs. Together, these factors significantly contribute to the inherently slower recombination rate observed in anatase-phase TiO₂ thin films, thereby enhancing their charge transport properties and overall performance in optoelectronic applications. Additionally, the lowest Rs values can also be found at sintering

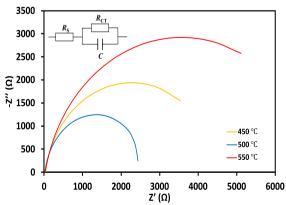


Fig. 5 Nyquist plot of the EIS responses of the TiO_2 thin films under light illumination.

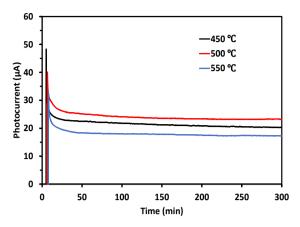


Fig. 6 Transient photocurrent graph ${\rm TiO_2}$ thin films at different sintering temperatures measured at 1.0 V versus Ag/AgCl with illumination.

Table 3 Charge transfer resistance and series resistance of TiO_2 thin films prepared at different sintering temperature

Temperature, °C	R_{CT} , $\mathbf{k}\Omega$	R _S , Ω
450	5.530	32.9
475	2.857	25.0
500	2.655	20.6
525	2.990	21.9
550	7.086	28.9
575	8.084	28.7

temperature of 500 °C. Low Rs value would allow minimal hysteresis in the perovskite solar device (Upama *et al.*, 2017).

Previous studies have used the TPC signal to reveal the charge extraction dynamics and have demonstrated that the TPV signal is a good measure of the charge carrier density in the device, such that the transient decay corresponds well with the recombination rate of the photogenerated charge. Transient photocurrent measurement has been extensively used by previous literatures to study the charge extraction dynamics of the thin film, in such manner that the recombination rate of the photogenerated charge can be represented by the transient decay (Wood et al., 2017). The transient photocurrent measurement of the spin-coated TiO2 thin films prepared at different sintering temperatures was determined by measuring the photocurrent time dependence under light illumination. In Figure 6, TiO₂-500 displayed the highest photocurrent density, followed by TiO₂-450 and TiO₂-550. Additionally, ΔQ, total charge extracted, can be represented as the area under the curve in Figure 6 (Wood et al., 2017). Thus, highest ΔQ is 500°C at 7.07 μ C, followed by 450°C the 550 °C, with 6.35 μ C and 5.23 μ C, respectively. The trend in Δ Q results is coincidently similar with the LSV and EIS results. All the TiO2 samples displayed a rather stable performance under 300 min of illumination.

4. Conclusion

In summary, TiO_2 thin films as ETL were fabricated at different sintering temperatures. The thin films displayed typical TiO_2

response in XRD where crystal structure of anatase was observed for samples sintered at 450 °C and 500 °C. The observed increase in both crystallite size and surface roughness at a sintering temperature of 550 °C suggests a significant microstructural transformation indicative of a phase transition from the anatase to the rutile crystal structure in TiO₂. Such changes are critical, as they influence the optical, electrical, and catalytic properties of the TiO₂ film, making them highly relevant in tailoring material performance for specific applications. TiO2-500 showed the highest photocurrent density and the lowest charge transfer and series resistance. Furthermore, the TiO₂ thin films were stable for 5 h of testing. Surface roughness was not a major influencing factor of photocurrent density rather than the anatase phase of the TiO2 thin film is more important. The higher sintering temperature of more than 550 °C introduced phase change to rutile in the TiO₂ thin film, which reduced photocurrent density and increased resistance. The thin film surface played an important role for the presence of photocatalytic sites; nevertheless, the internal lattice of the thin film will determine the photocurrent density and resistance of the thin film, which are crucial for highefficiency perovskite solar devices. This work offers a systematic evaluation of the ETL in terms of morphological and photoelectrochemical properties, which can be applied when selecting suitable material for ETL in perovskite solar devices.

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